

FORM PTO-1449(Modified)	ATTY. DOCKET NO. S1022/8544	SERIAL NO. Unassigned
LIST OF PATENTS AND PUBLICATIONS FOR APPLICANT'S INFORMATION DISCLOSURE STATEMENT	APPLICANT S1022/8544	
	FILING DATE Herewith	GROUP Unassigned

U.S. PATENT DOCUMENTS

Exam Init	Ref Des	Document No.	Date	Name	Class	Sub Class	FILING DATE If Appropriate
AJB		5,808,492	09/15/98	Chow	327	108	
CSB		5,974,241	10/26/99	Fusco	395	500.03	PRO \$692.297

FOREIGN PATENT DOCUMENTS

		Country & Doc. No. (11)	Pub. Date (43)		Class	Sub Class	Translation Yes No
<i>Q1B</i>		EP 0 700 008	03/06/96	Peters et al.	G06F	17/50	

OTHER ART

(Including Author, Title, Date, Pertinent Pages, Publication, Etc.)

		Standard Search Report from patent application No. GBA 9925655 filed October 29, 1999
CSB		IEEE Std 1164-1993: IEEE Standard Jmultivalue Logic System for VHDL Model Interoperability <u>(Std_logic_1164)</u> , 1993, IEEE, New York XP002135132, page 17

* a copy of this reference is not provided as it was previously cited by or submitted to the office in a prior application, Serial No. _____, filed _____, and relied upon for an earlier filing date under 35 U.S.C. 120 (continuation, continuation-in-part, and divisional applications).

EXAMINER	Cryptal J Barnes	DATE CONSIDERED	09 Sept 2007
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.